

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Basil LUI

Application No.: 09/762,589



10 / IDS
E. Willis
12-5-01

Group Art Unit: 2858

Filed: March 30, 2001

Docket No.: 108572

For: SEMICONDUCTOR DEVICE SIMULATION METHOD AND SIMULATOR

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.

2. The references were cited in a counterpart foreign application. The search report on the British priority application is attached for the Examiner's information.

3. Reference 1 was cited in a counterpart foreign application. An English language version of the PCT search report is attached for the Examiner's information.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

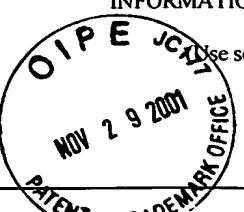
Thomas J. Pardini
Registration No. 30,411

JAO:TJP/zmc

Date: November 29, 2001

OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE	ATTY DOCKET NO. 108572	APPLICATION NO. 09/762,589		
INFORMATION DISCLOSURE STATEMENT  Use several sheets if necessary		APPLICANT(S) Basil LUI				
		FILING DATE March 30, 2001	GROUP 2858			
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)						
	1	Lui et al., "A new generation -recombination model for device simulation including the poole-frenkel effect and phonon-assisted tunneling", Solid-State Electronics, Vol. 41, No. 4, pp 575-583, 1997				
	2	Pelaz et al., "The Poole-Frenkel Effect in 6H-SiC Diode Characteristics", IEEE Transactions on Electron Devices, Vol. 41, No. 4, April 1994, pp. 587-591				
	3	Reisch, "Tunneling-Induced Leakage Currents in <i>pn</i> Junctions", Archiv fur Elektronik und Uebertragungstechnik, Vol. 44, No. 5, Sept.-Oct. 1990, pp. 368-382				
EXAMINER				DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						